New Application: Yoshinori SHIZUNO Filed: November 7, 2003 4 sheets of drawings FIGS.1A-4 RABIN & BERDO, P.C. Atty Docket: OKI 380

Customer NO.: 23995

1/4

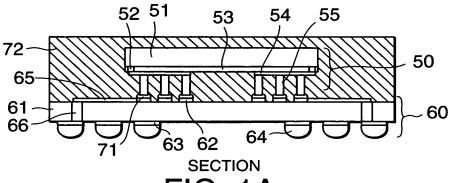
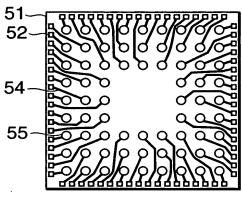
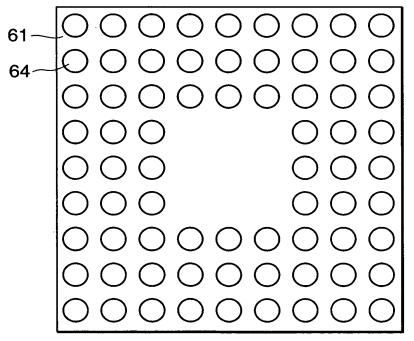


FIG. 1A



PLAN OF SEMICONDUCTOR CHIP

FIG. 1B



SEMICONDUCTOR DEVICE OF FIRST EMBODIMENT OF THE PRESENT INVENTION

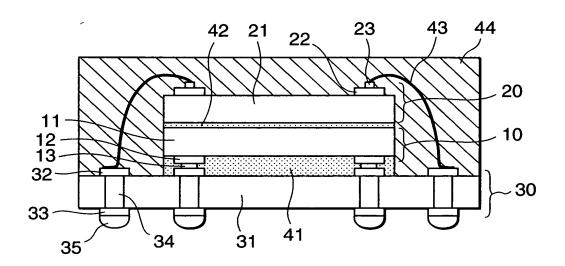
FIG. 1C

F-02ED0019

New Application: Yoshinori SHIZUNO Filed: November 7, 2003 4 sheets of drawings FIGS.1A-4 RABIN & BERDO, P.C.

Atty Docket: OKI 380 Customer NO.: 23995

2/4



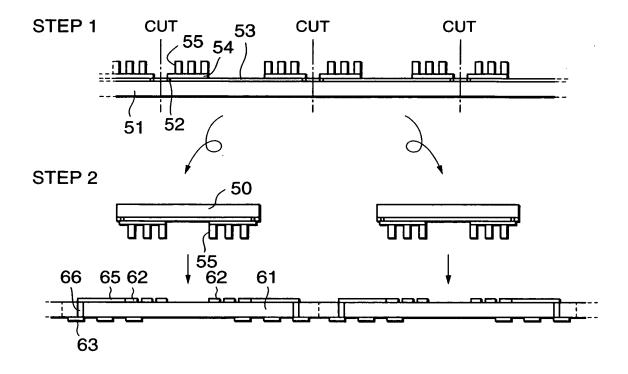
PRIOR-ART SEMICONDUCTOR DEVICE

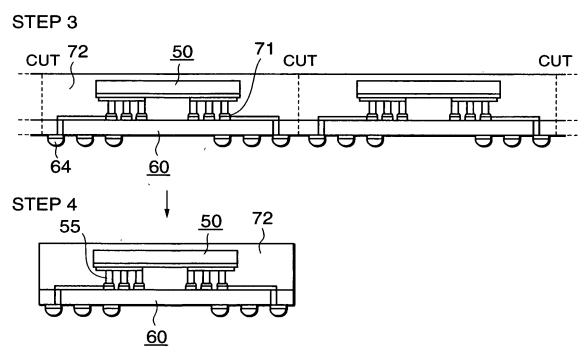
FIG. 2

New Application: Yoshinori SHIZUNO Filed: November 7, 2003 4 sheets of drawings FIGS.1A-4 RABIN & BERDO, P.C. Atty Docket: OKI 380

Customer NO.: 23995

3/4





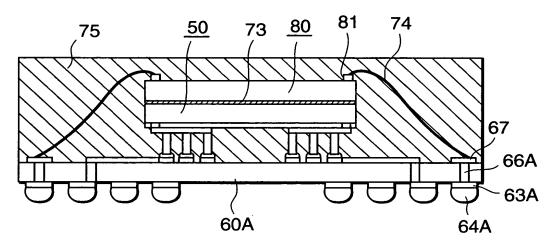
MANUFACTURING METHOD OF FIG. 1

FIG. 3

New Application: Yoshinori SHIZUNO Filed: November 7, 2003 4 sheets of drawings FIGS.1A-4 RABIN & BERDO, P.C. Atty Docket: OKI 380

Customer NO.: 23995

4/4



SEMICONDUCTOR DEVICE OF SECOND EMBODIMENT OF THE PRESENT INVENTION

FIG. 4